Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	871	(distributed near2 multiplex\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 13:50
L2	1	I1 with ((read or writ\$3)same clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 13:52
L3	11	l1 same ((read or writ\$3)same clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 14:05
L4	188	I1 and ((power or energy) near4 (sav\$4 or conserv\$5 or reduc\$5 or low\$3 or minimi\$6 or optimiz\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:39
L5	1778	(read near3 (power or energy))with (sav\$4 or conserv\$5 or optimi\$5 or minimiz\$4 or reduc\$5 or lower\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 14:03
L6	18732	((select\$5 or adapt\$5 or condition\$5 or control\$4)near4 output) with (latch or flipflop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 14:06
L7	53	I5 same (latch or flipflop or FF)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 14:43
L8	12617	((select\$5 or adapt\$5 or condition\$5 or control\$4)near4 output) near5 (latch or flipflop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 14:06

L9	0	15 same 18	US-PGPUB; USPAT; USOCR;	OR	OFF	2004/12/29 14:56
			EPO; JPO; DERWENT; IBM_TDB			
L10	45	I5 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:22
L11	41214	(modified or control\$4)near3 (latch or flipflop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:31
L12	3836	L11 with clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:23
L13	298	L12 with read	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:24
L14	157	L13 with output	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:24
L15	0	L12 with (read near3 active)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:24
L16	105	L12 and (read near3 active)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:26

L17	28	L16 and (clock adj5 phase)	US-PGPUB; USPAT; USOCR;	OR	OFF	2004/12/29 15:25
			EPO; JPO; DERWENT; IBM_TDB			
L18	0	L14 same (output adj open)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:26
L19	0	L14 and (output adj open)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:26
L20	26025	(modified or control\$4 or updated or changed)adj3 (latch or flipflop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:32
L21	25208	(modified or control\$4 or updated or changed)adj3 (latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:32
L22	2074	(modified or updated or changed)adj3 (latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:32
L23	2170	(modified or updated or changed or corrected)adj3 (latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:33
L24	1	I23 and (output with (clock with (read near2 active)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:37

L25	1	I23 and (output same (clock with (read near2 active)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:52
L26	3172	((retirement adj (payload adj array))or RPA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:39
L27	266	l26 and ((power or energy) near4 (sav\$4 or conserv\$5 or reduc\$5 or low\$3 or minimi\$6 or optimiz\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:41
L28	2	I26 and (((power or energy) near3 dissipat\$4) with read)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:41
L29	5	l26 and (read with ((power or energy) near4 (sav\$4 or conserv\$5 or dissipat\$4 or reduc\$5 or low\$3 or minimi\$6 or optimiz\$5)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2004/12/29 15:44
L30	8522	(read with ((power or energy) near4 (sav\$4 or conserv\$5 or dissipat\$4 or reduc\$5 or low\$3 or minimi\$6 or optimiz\$5)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF .	2004/12/29 15:44
L31	13	I30 and (output same (clock with (read near2 active)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 15:52
S1	20971	(read\$4 or access\$4)with (power near3 (dissipat\$4 or consum\$5 or conserv\$5 or sav\$4 or reduc\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:34

	1					
S2	10223	S1 same (memory or storage or array or ram or cache or cell or ram or rom)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 08:58
S3	6272	S2 and ((limit\$4 or control\$4 or restrict\$4 or disabl\$4 or ("not" near2 latch\$4))same (new or "same" or unchang\$4 or identical or different))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 09:37
S4	796	S2 same ((limit\$4 or control\$4 or restrict\$4 or disabl\$4 or ("not" near2 latch\$4))same (new or "same" or unchang\$4 or identical or different))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 09:08
S5	1285	S1 and ((clock near5 (gat\$4 or restrict\$4 or enabl\$4 or disabl\$4 or control\$4))same (RPA or read))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:45
S6	66	S5 and (glitch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:00
S7	95	S2 same ((clock near5 (gat\$4 or restrict\$4 or enabl\$4 or disabl\$4 or control\$4))same (RPA or read))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 09:14
S8	347	((select\$5 or adapt\$5 or condition\$5 or control\$4)near4 output)same (clock with (read near4 (active or enabl\$4)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 14:04
S9	70	S8 same (glitch or latch\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:03

S10	7	S8 and (glitch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 09:19
S11	6760	S2 and ((limit\$4 or control\$4 or restrict\$4 or disabl\$4 or ("not" near2 latch\$4) or qualif\$5 or gat\$4)same (new or "same" or unchang\$4 or identical or different))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 09:38
S12	1	S5 and (glitch)	EPO; JPO; DERWENT	OR	OFF	2004/12/29 10:52
S13	941	(((read\$4 or access\$4)near5 (activat\$4 or enabl\$4 or restrict\$4 or qualif\$5 or gat\$4)))with (new near5 (read or data))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:10
S14	8	S13 same (power near3 (dissipat\$4 or consum\$5 or conserv\$5 or sav\$4 or reduc\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:32
S15	2577	(superscalar near2 (microprocessor or processor or controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:37
S16	4	S15 same ((read\$4 or access\$4)with (power near3 (dissipat\$4 or consum\$5 or conserv\$5 or sav\$4 or reduc\$5)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:39
S17	20308	wallace.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF 	2004/12/29 10:38
S18	0	S17 and (superscalar near2 (microprocessor or processor or controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:38

S19	66570	steven.inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:38
S20	100	S19 and (superscalar near2 (microprocessor or processor or controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:50
S21	3	S20 and((read\$4 or access\$4)with (power near3 (dissipat\$4 or consum\$5 or conserv\$5 or sav\$4 or reduc\$5)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:43
S22	4	S20 and((read\$4 or access\$4)same (power near3 (dissipat\$4 or consum\$5 or conserv\$5 or sav\$4 or reduc\$5)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:42
S23	6	S20 and((power near3 (dissipat\$4 or consum\$5 or conserv\$5 or sav\$4 or reduc\$5)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:42
S24	105	(superscalar near2 (microprocessor or processor or controller)) and((read\$4 or access\$4)with (power near3 (dissipat\$4 or consum\$5 or conserv\$5 or sav\$4 or reduc\$5)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2004/12/29 13:49
S25	43	S24 and ((clock near5 (gat\$4 or restrict\$4 or enabl\$4 or disabl\$4 or control\$4))same (RPA or read))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:46
S26	0	S24 and ((reduc\$5 or low\$4 or minimiz\$6)near5 (read near2 power))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:48

S27	2	S24 and ((reduc\$5 or low\$4 or minimiz\$6)same (read near2 power))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:49
S28	1246	((reduc\$5 or low\$4 or minimiz\$6 or optimiz\$5)with (read near2 power))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:49
S29	957	((reduc\$5 or low\$4 or minimiz\$6 or optimiz\$5)near6 (read near2 power))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:49
S30	0	S29 and (superscalar near2 (microprocessor or processor or controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:50
S31	470	S29 and ((microprocessor or processor or controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:50
S32	0	S31 and (glitch)	EPO; JPO; DERWENT	OR	OFF	2004/12/29 10:52
S33	25	S31 and (glitch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/29 10:52

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> 4 Reducing reorder buffer complexity through selective operand cachi Kucuk, G.; Ponomarev, D.T.; Ergin, O.; Ghose, K.; Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 2003

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[Abstract]

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5 Deterministic clock gating for microprocessor power reduction

Hai Li; Bhunia, S.; Chen, Y.; Vijaykumar, T.N.; Roy, K.;

The Ninth International Symposium on High-Performance Computer Architectu 2003. HPCA-9 2003. Proceedings. , 8-12 Feb. 2003

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6 Instruction flow-based front-end throttling for power-aware highperformance processors

Baniasadi, A.; Moshovos, A.;

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[Abstract] [PDF Full-Text (524 KB)] **IEEE CNF**

7 Energy-efficient instruction dispatch buffer design for superscalar processors

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8 A technique to determine power-efficient, high-performance supersc processors

Conte, T.M.; Menezes, K.N.P.; Sathaye, S.W.;

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Abella, J.; Canal, R.; Gonzalez, A.;

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[Abstract] [PDF Full-Text (1616 KB)] **IEEE JNL**

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Lev, L.A.; Charnas, A.; Tremblay, M.; Dalal, A.R.; Frederick, B.A.; Srivatsa, C. Greenhill, D.; Wendell, D.L.; Duy Dinh Pham; Anderson, E.; Hingarh, H.I.; Razzack, I.; Kaku, J.M.; Shin, K.; Levitt, M.E.; Allen, M.; Ferolito, P.A.; Bartolo R.I.; Yu, R.K.; Melanson, R.J.; Shah, S.I.; Nguyen, S.; Mitra, S.S.; Reddy, V.; Ganesan, V.; de Lange, W.J.;

Solid-State Circuits, IEEE Journal of, Volume: 30, Issue: 11, Nov. 1995 Pages:1227 - 1238

[Abstract] [PDF Full-Text (1668 KB)] **IEEE JNL**

13 Reducing power requirements of instruction scheduling through dynamic allocation of multiple datapath resources

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[Abstract] [PDF Full-Text (1488 KB)] **IEEE CNF**

14 A circuit-level implementation of fast, energy-efficient CMOS comparators for high-performance microprocessors

Ergin, O.; Ghose, K.; Kucuk, G.; Ponomarev, D.;

Computer Design: VLSI in Computers and Processors, 2002. Proceedings. 200 IEEE International Conference on , 16-18 Sept. 2002

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15 Simplifying instruction issue logic in superscalar processors

Sato, T.; Arita, I.;

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[Abstract] [PDF Full-Text (212 KB)] IEEE CNF

15 Inherently lower-power high-performance superscalar architecture Zyuban, V.V.; Kogge, P.M.;

Computers, IEEE Transactions on , Volume: 50 , Issue: 3 , March 2001 Pages: 268 - 285

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